

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8421	(concurrent\$2 simultaneous\$2 parallel synchronous\$2) with (edit\$3 writ\$3 chang\$3) with (user designer engineer\$3 operator)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 11:47
L2	894	L1 and "707"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 12:58
L3	19	(pcb (printed adj circuit)) and L2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 13:05
L5	9	2 and "703"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 12:58
L6	0	(pcb (printed adj circuit)) and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 13:05
L7	0	circuit and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 13:05
L9	31	((("7240309") or ("5806058") or ("6530065") or ("6594799") or ("6678877") or ("6751781") or ("6931369") or ("7143341") or ("7219311") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095") or ("7240309") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095"))). PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/13 13:43
L10	10	(concurrent\$2 simultaneous\$2 parallel synchronous\$2) and L9	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 13:10
L11	14	(printed near2 (circuit or board or assembly)) same (edit\$3 with (simultaneous\$2 or synchronous\$2 or concurrent\$2 or parallel))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 14:54

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L12	157	(printed near2 (circuit or board or assembly)) and (edit\$3 with (simultaneous\$2 or synchronous\$2 or concurrent\$2 or parallel))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 14:49
L13	45	((edit\$3 alter\$3 or modify\$3 or chang\$3) with (simultaneous\$2 or synchronous\$2 or concurrent\$2)) with (user or designer or operator or engineer)) and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 14:44
L14	5	((printed near2 (circuit or board or assembly)) and (edit\$3 with (simultaneous\$2 or synchronous\$2 or concurrent\$2 or parallel))).clm.	US-PGPUB	OR	ON	2007/11/13 14:49
L15	1185	716/8.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 15:07
L16	652	716/9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 15:15
L17	1358	716/10.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 15:27
L18	1109	716/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 15:27

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S75	31	((("7240309") or ("5806058") or ("6530065") or ("6594799") or ("6678877") or ("6751781") or ("6931369") or ("7143341") or ("7219311") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095") or ("7240309") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/13 08:04
S76	2	("20040210854").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/08 18:34
S77	1	artwork and S76	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/08 18:34
S78	2	("7246055").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/12 16:11
S79	1	synchroniz\$5 and S78	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 16:33
S80	31	((("7240309") or ("5806058") or ("6530065") or ("6594799") or ("6678877") or ("6751781") or ("6931369") or ("7143341") or ("7219311") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095") or ("7240309") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/12 16:33
S81	4	synchroniz\$5 and S80	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 16:44

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S82	8	synchroniz\$5 and (PFEIL-CHARLES.in. SMITH-EDWIN.in. PETUNIN-VLADIMIR.in. POTTS-HENRY.in. NATARAJAN-VENKAT.in.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 16:48
S83	33	synchroniz\$5 with (edit\$3 writ\$3) same (pcb (printed adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 16:56
S84	1458	synchroniz\$5 with (edit\$3 writ\$3) and (pcb (printed adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 16:59
S85	18	S84 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:01
S86	3890	synchroniz\$5 with (edit\$3 writ\$3) same (concurrent simultaneous\$2 synchronous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:01
S87	8	S86 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:32
S88	96	S86 and "709"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:42
S89	10	S86 and "703"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:32
S90	18	S88 and board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/12 17:43
S91	31	((("7240309") or ("5806058") or ("6530065") or ("6594799") or ("6678877") or ("6751781") or ("6931369") or ("7143341") or ("7219311") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095") or ("7240309") or ("7246055") or ("20020120858") or ("20040199891") or ("20040268283") or ("20050108663") or ("2003088095"))). PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/11/13 08:05

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S92	10	S91 and (concurrent\$2 simultaneous\$2 parallel synchronous\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 08:17
S93	3	Kohli-Vikas.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 08:09
S94	167	cadence.as. and ((concurrent\$2 simultaneous\$2 parallel synchronous\$2) with design)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 08:23
S95	13	S94 and ((concurrent\$2 simultaneous\$2 parallel synchronous\$2) with (edit\$3 writ\$3 chang\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 08:22
S96	8421	(concurrent\$2 simultaneous\$2 parallel synchronous\$2) with (edit\$3 writ\$3 chang\$3) with (user designer engineer\$3 operator)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 10:14
S97	8	cadence.as. and S96	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 08:28
S98	69	S96 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 09:38
S99	27	(pcb or (printed adj circuit)) and S98	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 09:38
S100	8421	(concurrent\$2 simultaneous\$2 parallel synchronous\$2) with (edit\$3 writ\$3 chang\$3) with (user designer engineer\$3 operator)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 13:09
S101	69	S100 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 11:40
S102	27	(pcb (printed adj circuit)) and S101	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 11:47
S103	894	S100 and "707"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 11:40

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S10 4	19	(pcb (printed adj circuit)) and S103	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/11/13 11:41
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